

MULTI-LAYER INTEGRATED SEMICONDUCTOR STRUCTURE

Rafael Reif, et al.

MIT-136AUS

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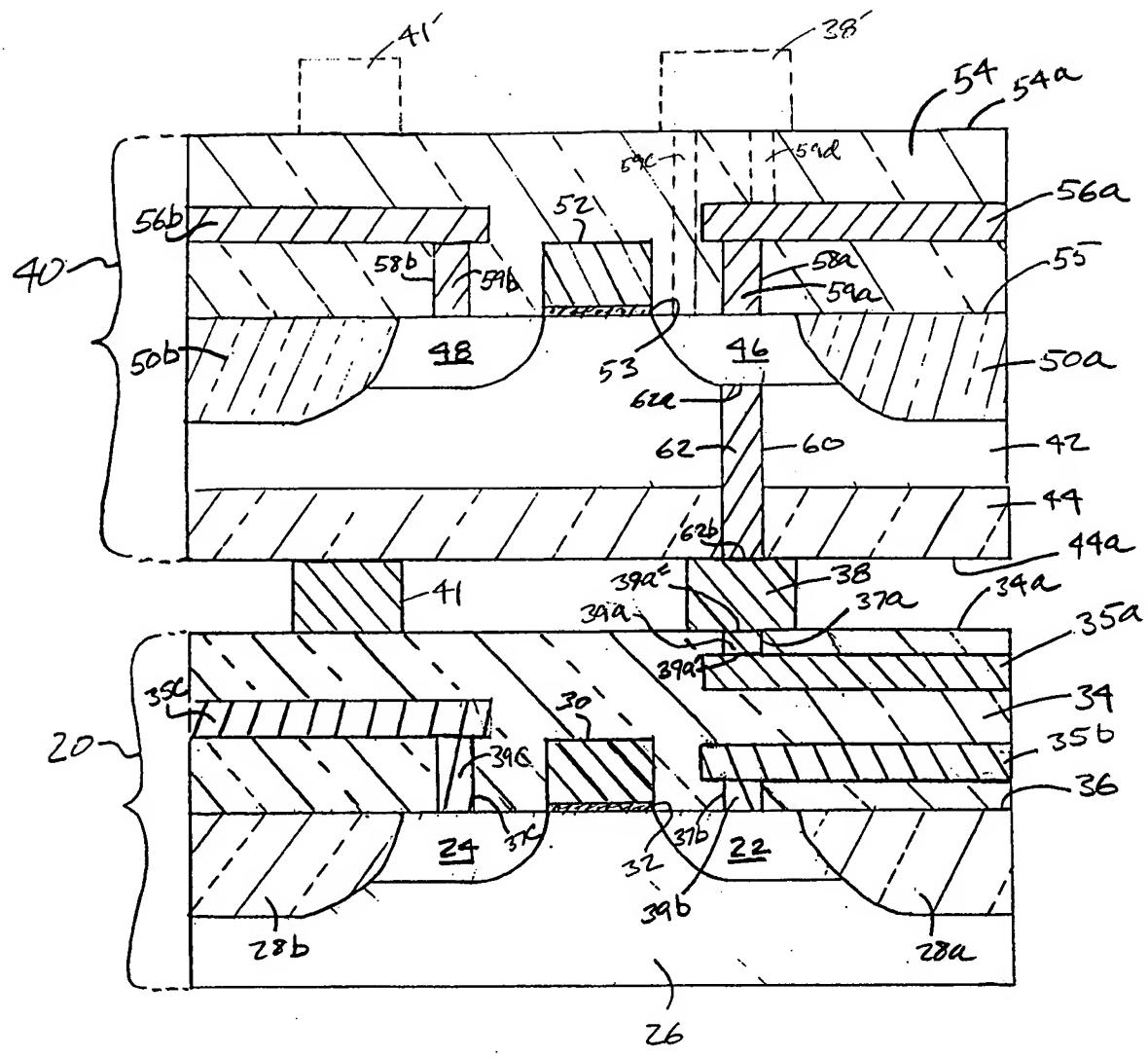


Fig. 1

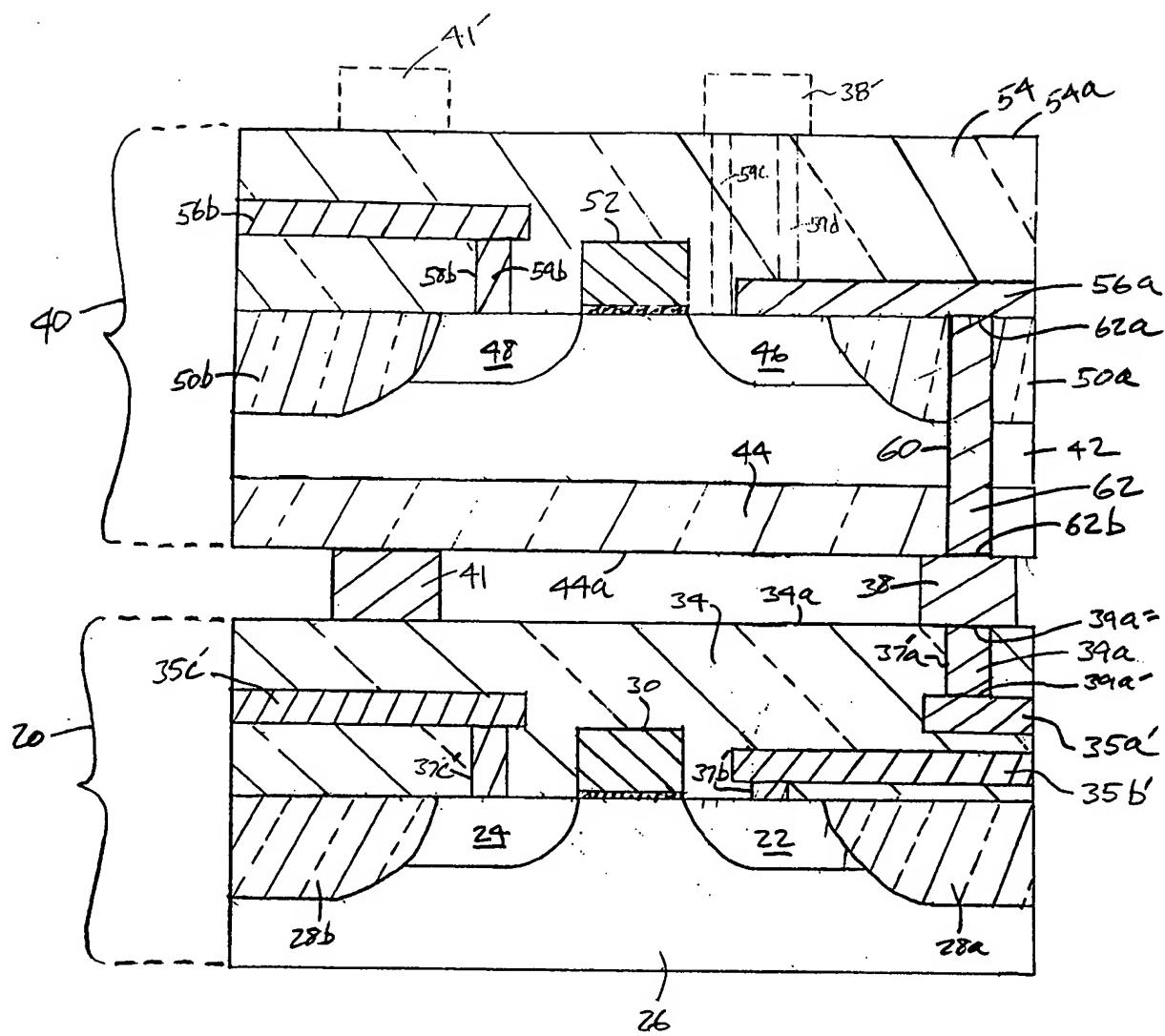


Fig. 2

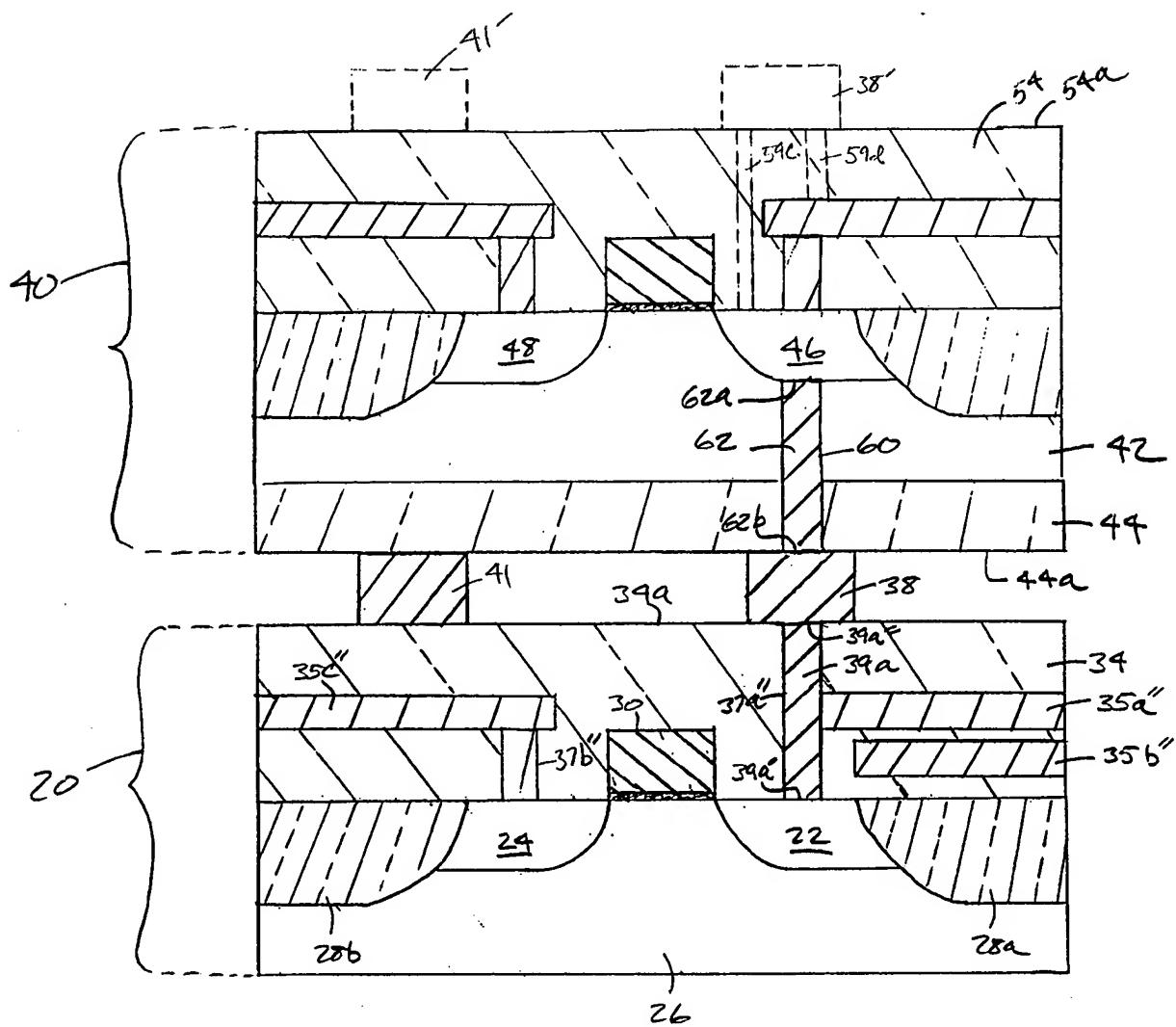
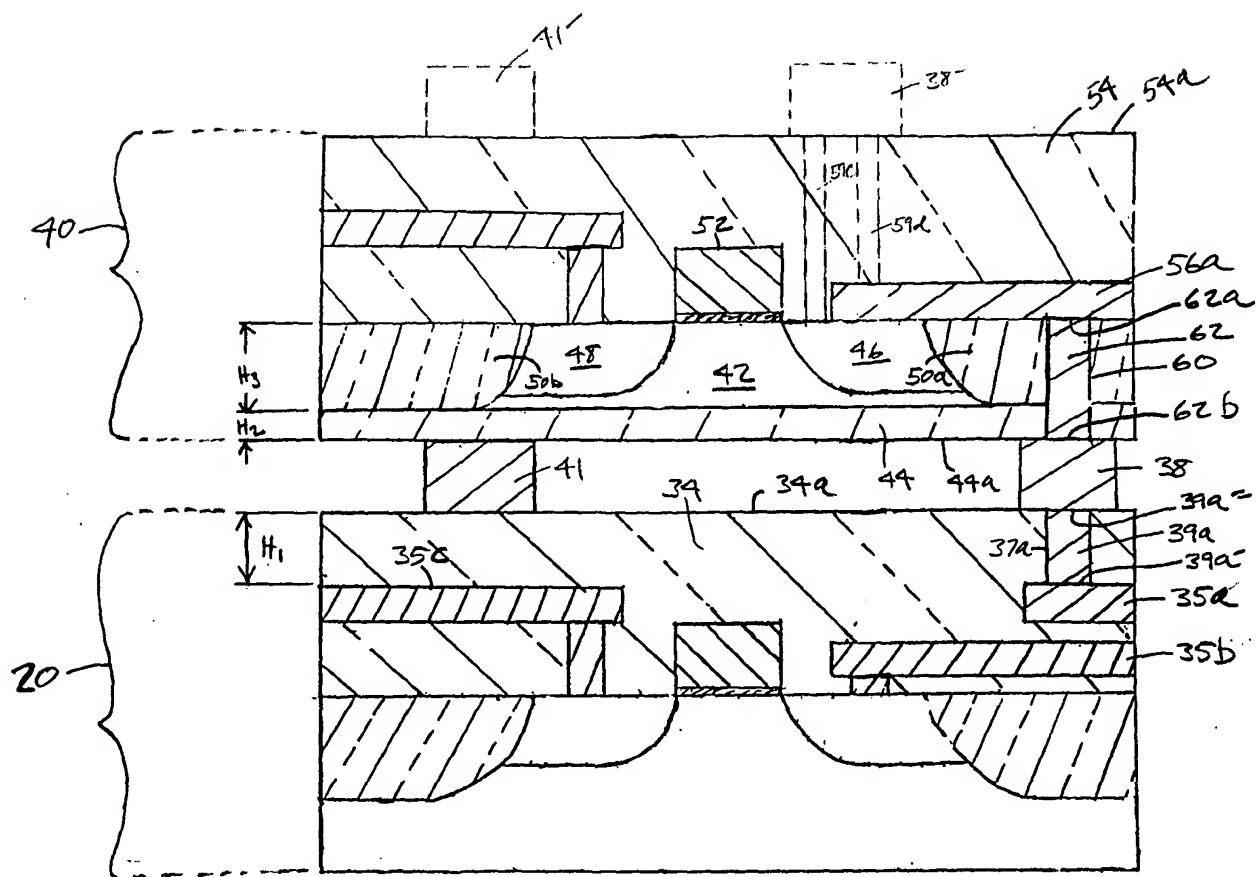


Fig. 3



10d

Fig. 4

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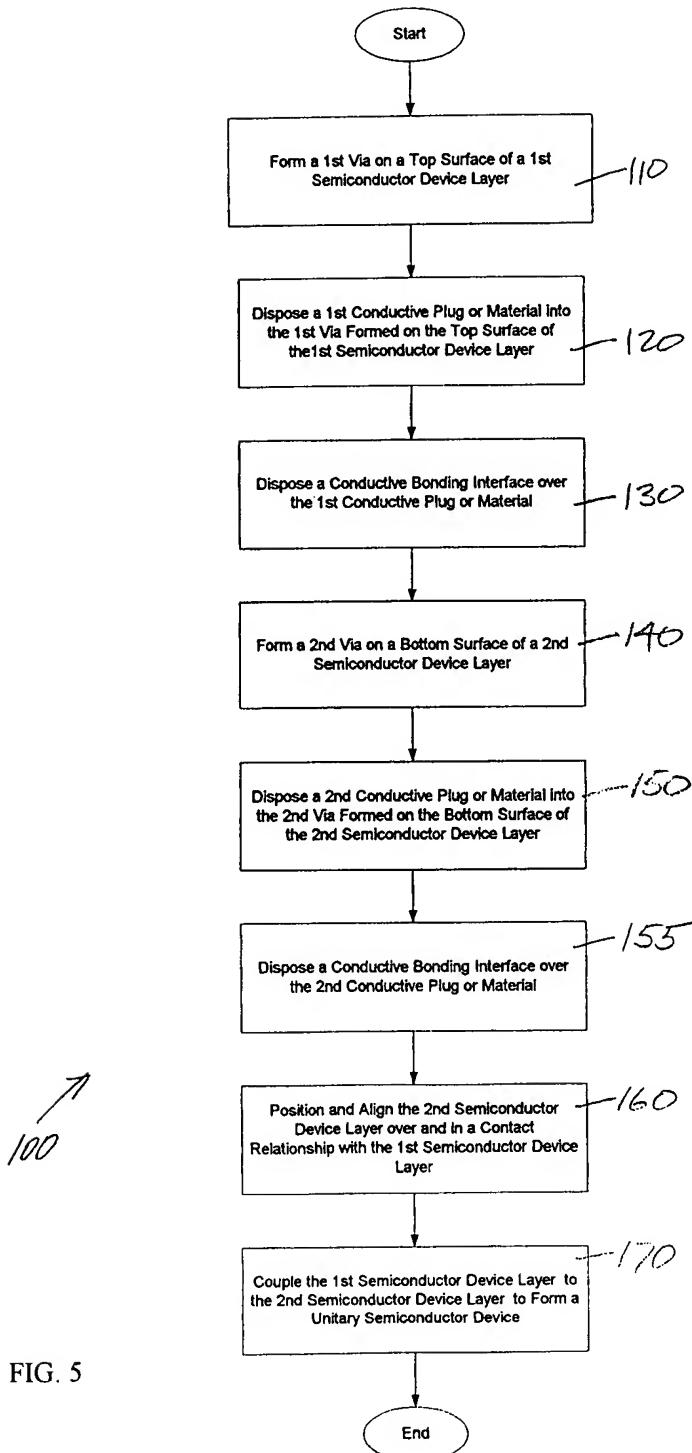
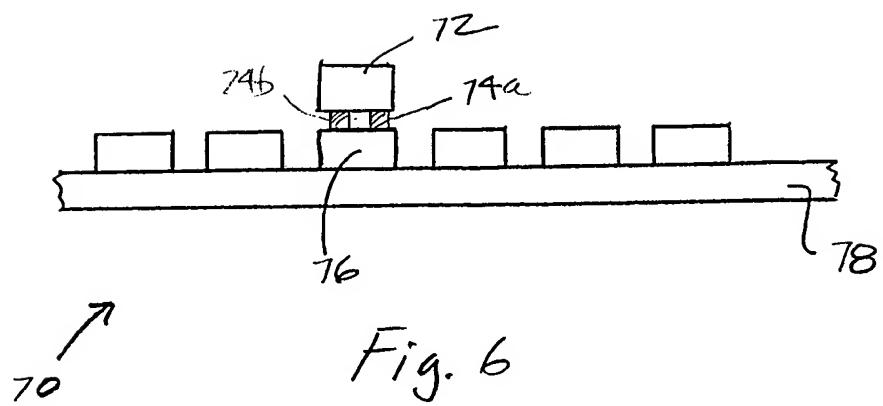


FIG. 5



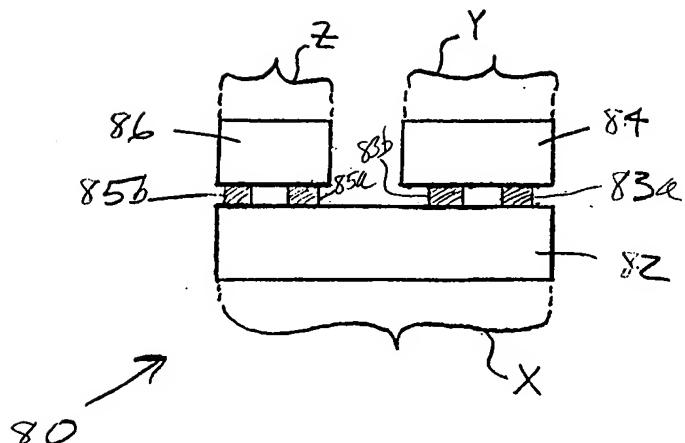


Fig. 7